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Application Number	10/767,069
Filing Date	January 30, 2004
First Named Inventor	Sheji Shukuri
Art Unit	2824
Examiner Name	D. MENZ
Attorney Docket Number	501.42645VX1

U.S. PATENT DOCUMENTS

Examiner Initials'	Cite No. ¹	Document Number		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code ³ (if known)			
DS		US- 2004/0119107	A1	08-24-2004	Hisamoto et al	
		US- 2004/0155234	A1	08-12-2004	Ishimaru et al	
		US- 2004/0232471	A1	11-25-2004	Shukuri	(= to WO 03/028112 A1)
		US- 6,248,633	B1	08-19-2001	Ogura et al	
		US- 6,255,166	B1	07-03-2001	Ogura et al	
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FOREIGN PATENT DOCUMENTS

Examiner Initials'	Cite No. ¹	Foreign Patent Document		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ²	Number ⁴ - Kind Code ⁵ (if known)				
DS		EP	1416540 A1	05-06-04	T. Tanaka et al	(=WO 03/012878 A1)	
		WO	03/012878 A1	02-13-03	T. Tanaka et al		
		WO	03/028112 A1	04-03-03	S. Shukuri et al		
DN		WO	03/021666 A1	03-13-03	S. Shukuri et al		

Examiner Signature	<i>DS</i>	Date Considered	3/16/06
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First Named Inventor	Sheji Shukuri
Group Art Unit	2824
Examiner Name	D. MENZ
Attorney Docket Number	501.42645VX1

NON-PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
D2		NOZAKI, Takaaki et al, "A 1-Mb EEPROM with MONOS Memory Cell for Semiconductor Disk Application," IEEE Journal of Solid State Circuits, Vol. 26, NO. 4, April 1991 (pgs. 497-501).	

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3/16/06

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